

# Call for Papers

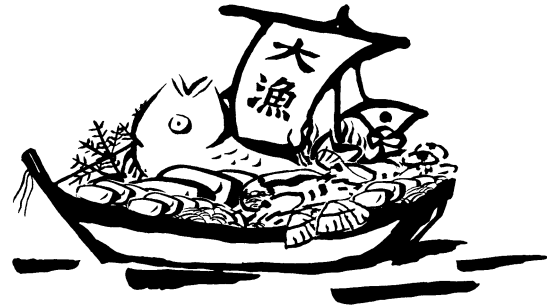
# SASIMI 2003

THE 11TH WORKSHOP ON  
SYNTHESIS AND SYSTEM INTEGRATION  
OF MIXED INFORMATION TECHNOLOGIES

**April 3-4, 2003**

**International Conference Center Hiroshima, Japan**

In cooperation with IEEE Hiroshima Section  
The Institute of Electronics, Information and Communication Engineers (IEICE)  
Information Processing Society of Japan (IPSJ)  
Semiconductor Technology Academic Research Center (STARC)



## **Aims of the Workshop:**

This workshop will provide an interchange forum on system design methodology and CAD/DA technologies for LSI and VLSI. Presentations on theoretical aspects, practical issues, case studies and applications are encouraged. The workshop gives an opportunity for presentation and discussion of advanced work and research. Works in progress and new ideas are also welcome. The workshop will provide an interchange forum on system design.

## **Areas of Interest include, but are not limited to:**

- Layout/Logic/Behavioral Synthesis
- Test, Verification and Simulation
- System Design and Design Experiences
- Embedded Software Design
- Analog and Mixed-Signal Design
- New Design Methodologies (HW/SW Codesign, MEMS, etc.)

## **Submission of Papers:**

Prospective authors are invited to submit extended abstract or full length manuscript electronically in PDF format by November 15, 2002. Detailed instructions for paper submission are available at the URL below.

**Submission due date: November 15, 2002**

**Notification of Acceptance: January 17, 2003**

**Final paper due date: February 21, 2003**

## **Keynote:**

- Prof. Hugo De Man (IMEC / K. U. Leuven, Belgium)  
“Designing Nano-scale Systems for the Ambient Intelligence World”

## **Invited Talks:**

- Dr. Raul Camposano (Synopsys, USA)  
“The Future of IC Design: A Design Technology Point of View”
- Prof. Atsushi Iwata (Hiroshima Univ., Japan)  
“Advanced Design for Analog-RF and Digital Mixed LSIs — Crosstalk Noise Evaluation and Reduction —”
- Prof. Yoshio Nishi (Stanford Univ., USA)  
“Future of Integrated Circuits Technology for the Internet Era”
- Prof. Ralph Otten (Tech. Univ. Eindhoven, Germany)  
“Wire planning for timing closure”
- Prof. Rob A. Rutenbar (Carnegie Mellon Univ., USA)  
“The MARCO Focus Research Center for Circuits Systems and Software (C2S2): Life at the End of Technology Scaling”
- Prof. Wayne Wolf (Princeton Univ., USA)  
“Application-Specific Networks-on-Chips”

## **Panel Discussion:**

- Tentative title: Everything is (Re-) Programmable and (Re-) Configurable:  
How Much “Hardware” is Still Needed ?  
Organizer and Moderator: Prof. Wolfgang Rosenstiel (Tuebingen Univ., Germany)

## **Conference Venue:**

Hiroshima lies in the southwest of Japan’s main island. The center of the city is situated in a delta with six rivers, surrounded by mountains and sea. In spring, riverside area, including the convention center and Hiroshima Castle, is colored with beautiful cherry blossoms. In 1996, the A-Bomb Dome and Itsukushima Shrine were registered on the UNESCO’s World Heritage List.

## **For More and Latest Information:**

<http://www.arch.ce.hiroshima-cu.ac.jp/sasimi/>

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