

Final Call for Papers

SASIMI 2004

THE 12TH WORKSHOP ON
SYNTHESIS AND SYSTEM INTEGRATION
OF MIXED INFORMATION TECHNOLOGIES

October 18-19, 2004

Kanazawa Excel Hotel Tokyu, Kanazawa, Japan

In cooperation with IEEE Nagoya Section
The Institute of Electronics, Information and Communication Engineers (IEICE)
Information Processing Society of Japan (IPSJ)
Semiconductor Technology Academic Research Center (STARC)



Aims of the Workshop:

This workshop will provide an interchange forum on system design methodology and CAD/DA technologies for LSI and VLSI. Presentations on theoretical aspects, practical issues, case studies and applications are encouraged. The workshop gives an opportunity for presentation and discussion of advanced work and research. Works in progress and new ideas are also welcome.

Areas of Interest include, but are not limited to:

- Layout/Logic/Behavioral Synthesis
- Embedded Software Design
- Test, Verification and Simulation
- Analog and Mixed-Signal Design
- System Design and Design Experiences
- New Design Methodologies (HW/SW Codesign, MEMS, etc.)

Submission of Papers:

Prospective authors are invited to submit extended abstract or full length manuscript electronically in PDF format by June 4, 2004. Detailed instructions for paper submission are available at the URL below.

Submission due date: June 4, 2004

Notification of Acceptance: July 30, 2004

Final paper due date: September 3, 2004

Highlights:

Keynote:

- Prof. Chenming Hu (UCB/TSMC, USA/Taiwan)
“Future Directions of Silicon Devices”

Invited Talks:

- Dr. Sani Nassif (IBM, USA)
“Model to Hardware Closure for nm Generation Technologies”
- Prof. Masahiko Yoshimoto (Kobe Univ., Japan)
“Low Power VLSI Video Compression Processor”
- Dr. Rudy Lauwereins (IMEC, Belgium)
“A Vision towards an Ambient Intelligent Environment and the Associated System Level Design Challenges”
(tentative)

Panel Discussion:

- Title: “Life at the End of Technology Scaling” (tentative)
- Organizer and Moderator: Prof. Rob Rutenbar (CMU, USA)

For More and Latest Information:

<http://www.arch.ce.hiroshima-cu.ac.jp/sasimi/>

Conference Venue:

Kanazawa is located on the west coast of Japan's main island, almost in the geographical center of Japan. The city is surrounded by the Japan Alps, Hakusan National Park and Noto Peninsula National Park. Kanazawa is a castle town that was ruled over by the Maeda family for three centuries. Since many artists and craftsmen were invited to this area, it achieved a high level of craftsmanship.



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