

# Call for Participation

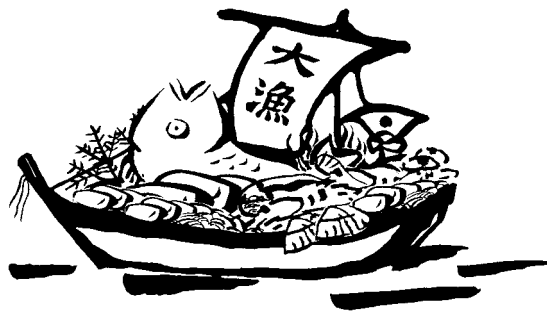
# SASIMI 2004

THE 12TH WORKSHOP ON  
SYNTHESIS AND SYSTEM INTEGRATION  
OF MIXED INFORMATION TECHNOLOGIES

**October 18-19, 2004**

**Kanazawa Excel Hotel Tokyu, Kanazawa, Japan**

In cooperation with IEEE Nagoya Section  
The Institute of Electronics, Information and Communication Engineers (IEICE)  
Information Processing Society of Japan (IPSI)  
Semiconductor Technology Academic Research Center (STARC)



## About SASIMI 2004:

SASIMI 2004 will provide an interchange forum on system design methodology and CAD/DA technologies for VLSI. Many researchers of Asia, U.S. and European countries in the area of hardware design methodology have supported this workshop and made SASIMI one of the most famous events in this research area. Researchers in the field of system design, VLSI design, VLSI CAD, and VLSI technologies are welcome to this workshop.

## Keynote:

- Prof. Chenming Hu (UCB/TSMC, USA/Taiwan)  
“Future Directions of Silicon Devices”

## Invited Talks:

- Dr. Sani Nassif (IBM, USA)  
“Model to Hardware Closure for nm Generation Technologies”
- Prof. Masahiko Yoshimoto (Kobe Univ., Japan)  
“Low Power VLSI Video Compression Processor”
- Dr. Rudy Lauwereins (IMEC, Belgium)  
“A Vision towards an Ambient Intelligent Environment and the Associated System Level Design Challenges”
- Prof. Philip Wong (Stanford Univ., USA)  
“Nanodevices beyond Silicon: Device and Circuit Implications”
- Prof. Chein-Wei Jen (ITRI, Taiwan)  
“A DSP Core for Portable Multimedia Application”
- Prof. Jan Rabaey (UCB, USA)  
“Ultra-Low Power Design: The Road to Disappearing Electronics”

## Regular Poster Sessions:

This workshop is designed to maximize interaction and informal interchange. While full papers are to be published for all contributors, a majority of the presentations are of a poster-board format. That is, each author gives a very short introduction followed by an extended period for informal one-to-one discussions. In total, 68 papers are presented in sessions including System Level Design, Logic Level Design, Physical Design, and Design Experiences.

## Advance Registration & Hotel Reservation:

**Advance Registration:** All participants including invited speakers, panelists, moderators, and session chairs are required to make registration. Advance registration deadline is **September 17, 2004**.

**Hotel Reservation:** Application for hotel reservation should be directly sent to Nippon Travel Agency (NTA) Kanazawa Travel Branch via the advance registration & hotel reservation page linked from the URL below by **September 17, 2004**.

## For More and Latest Information:

<http://www.arch.ce.hiroshima-cu.ac.jp/sasimi/>

## Conference Venue:

Kanazawa is located on the west coast of Japan's main island, almost in the geographical center of Japan. The city is surrounded by the Japan Alps, Hakusan National Park and Noto Peninsula National Park. Kanazawa is a castle town that was ruled over by the Maeda family for three centuries. Since many artists and craftsmen were invited to this area, it achieved a high level of craftsmanship.



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## Questions should be directed to:

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