

Call for Papers

SASIMI 2006

THE 13TH WORKSHOP ON
SYNTHESIS AND SYSTEM INTEGRATION
OF MIXED INFORMATION TECHNOLOGIES

April 3-4, 2006

Nagoya University, Nagoya, Japan

<http://www.sasimi.jp/>

In cooperation with IEEE Nagoya Section

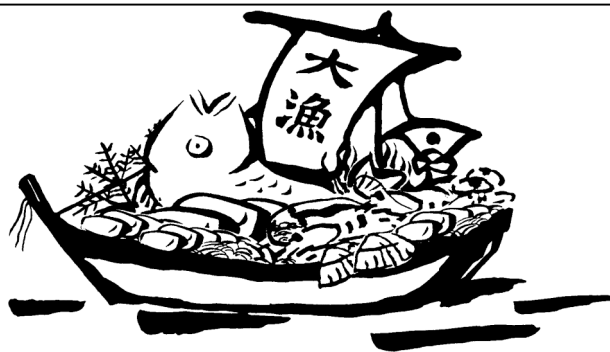
The Institute of Electronics, Information and Communication Engineers (IEICE)

Information Processing Society of Japan (IPSJ)

Semiconductor Technology Academic Research Center (STARC)

21st Century COE: Information Nano-Devices Based on Advanced Plasma Science

21st Century COE: Intelligent Media Integration for Social Infrastructure



Aims of the Workshop

This workshop will provide an interchange forum on system design methodology and CAD/DA technologies for VLSIs. Presentations on theoretical aspects, practical issues, case studies and applications are encouraged. The workshop gives an opportunity for presentation and discussion of advanced work and research. Works in progress and new ideas are also welcome.

Areas of Interest include, but are not limited to:

- Layout/Logic/Behavioral Synthesis
- Test, Verification and Simulation
- System Design and Design Experiences
- Embedded Software Design and HW/SW Codesign
- Analog and Mixed-Signal Design
- New Design Methodologies (Reconfigurable Systems, MEMS, etc.)

Submission of Papers

Prospective authors are invited to submit extended abstract or full-length manuscript electronically in PDF format by November 11, 2005, at the above URL. Detailed instructions for paper submission will be available at the web site.

Submission due date: **November 11, 2005**

Notification of Acceptance: **January 13, 2006**

Final paper due date: **February 17, 2006**

Highlights

Keynotes

- Mr. T. Shigematsu (Executive Director, Toyota Motor Corporation)
“Next Generation of Car Electronics” (*tentative*)
- Prof. Shankar Sastry (Director of CITRIS, UC Berkeley)
“Role of Semiconductor and IT in & after Late CMOS era; IT for Happiness”

Invited Talks

- Prof. Heinrich Meyr (RWTH Aachen)
- Dr. Andreas Kuehlmann (Cadence Berkeley Labs.)
- Prof. Vojin G. Oklobdzija (UC Davis)
“Digital Circuit Optimization for Low Energy Computing” (*tentative*)
- Prof. Rangunathan Rajkumar (CMU)
- Prof. Hideki Asai (Shizuoka Univ.)

Panel Discussion

- Organized by Mr. Takahide Inoue (UC Berkeley)

Conference Venue

Located in the heart of the main island of Japan, Nagoya is a historical city with the beautiful Nagoya Castle known for a pair of golden dolphins on its roof. Nagoya is also an industrial city with world-famous motor companies. Central Japan International Airport that is only 30 minute away from the downtown Nagoya offers a number of international flights from/to the world.



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