Call for Participation
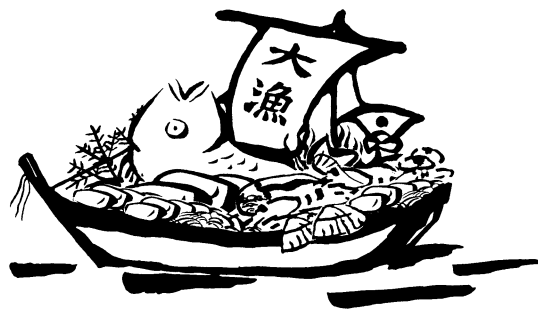# SASIMI 2015

THE 19TH WORKSHOP ON
SYNTHESIS AND SYSTEM INTEGRATION
OF MIXED INFORMATION TECHNOLOGIES

March 16–17, 2015
Evergreen Resort Hotel, Yilan, Taiwan

Aims of the Workshop

This workshop provides an interchange forum on system design, design experiences, EDA, and design methodologies for both of industry and academy. Many researchers of Asia, U.S. and European countries in the area of hardware design methodology have supported this workshop and made SASIMI one of the most famous events in this research area. The workshop gives a good opportunity for discussion on advanced work and research. Researchers in the field of system design, VLSI design, VLSI CAD, and VLSI technologies are welcome to this workshop.

Keynote Speeches

- Vijaykrishnan Narayanan (PSU, USA),
  "Influence of Emerging Devices in Revitalizing Electronic Systems Design"
- Ulf Schlichtmann (TUM, Germany), "Reliability and Robustness – Design and EDA to the Rescue!"

Invited Talks

- Hsien-Hsin Sean Lee (TSMC, Taiwan),
  "IC Design Challenges and Opportunities in Advanced Process Nodes"
- Tomohiro Yoneda (NII, Japan), "A New Approach to Synthesis of Transition Signaling Asynchronous Circuits"
- Yiyu Shi (Missouri S&T, USA),
  "Through-Silicon-Via Inductor based DC-DC Converters: The Marriage of the Princess and the Dragon"

Panel Discussion

"Circuit Reliability: Major Roadblock in Future Technology?"

- Organizer: Tsung-Yi Ho (NCTU, Taiwan)
- Moderator: Ing-Chao Lin (NCKU, Taiwan)
- Panelists: Vijaykrishnan Narayanan (PSU, USA), Ulf Schlichtmann (TUM, Germany), Tomohiro Yoneda (NII, Japan), Yiyu Shi (Missouri S&T, USA)

Regular Poster Sessions

This workshop is designed to maximize interaction and informal interchange. While full or short papers are to be published for all contributors, a majority of the presentations are of a poster-board format. That is, each author gives a very short introduction followed by an extended period for informal one-to-one discussions. Many "fresh" papers are to be presented in regular poster sessions on the topics including System Level Design, Logic Design, Physical Design, and Design Experiences.

Registration

The deadline for advance registration (via WWW) is February 13, 2015.

Conference venue:
Jiaosi, Yilan

Questions should be directed to:

SASIMI 2015, c/o Prof. Juinn-Dar Huang

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1001 University Road, Hsinchu 300, Taiwan

E-mail: sasimi15@sasimi.jp

For More and Latest Information: `http://sasimi.jp/`