Voice Learning of Reservoir Computing Architecture using Ternary Content Addressable Memory with Individuality

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Abstract— With the rapid progress in artificial intelligence (AI) technology, the number of machines that have been designed to interact with human beings has been steadily increasing. However, the responses of such machines to human interactions are often excessively uniform. The purpose of our study is to incorporate the variations that occur during chip manufacturing into machine learning and give own individuality to AI-based robots. In this paper, reservoir computing architecture using Ternary Content Addressable Memory with Individuality is developed and learning is performed using voice data, which is a complicated waveform. It is found that the error of the average of all data between 10 chips is 140% at the maximum. Voice data learning results have individuality outputs.

I. INTRODUCTION

Artificial intelligence (AI) technology, which has advanced rapidly, is expected to contribute to resolving a wide variety of social problems in fields such as human relationships, overtime work, and decline in production capacities. For example, AI-implemented robots have been developed to provide care for elderly people and serve guests in restaurants [1], [2]. However, the communications expressed by such robots in response to human inputs are often excessively uniform because they are designed to fulfill their roles precisely as programmed. Furthermore, AI-based robots can acquire individuality after learning process, however, teaching and learned data can be copied to other robots. It is difficult to seem to obtain personal like a human.

To overcome these problems, a Ternary Content Addressable Memory with Individuality (ITCAM) [3]-[5], has been developed for generating search result with LSI intrinsic manufacturing variations. In this study, a Reservoir Computing (RC) machine learning architecture [6] is combined with the ITCAM for realizing Individual robots. The RC is easy to make implement to hardware and not to be changed the weight in the middle layer. The proposed architecture is called the ITCAM-based Reservoir Computing Architecture (IRC) [7]. For verifying the IRC capability, voice learning process implements on the IRC with 10 FPGA chips.

II. Related work

This paper shows results some reservoir computing related works. The RC is proposed early 2000s [8]. This architecture can learn time series data, such as audio waveform. Recently, the RC gains exposure for improving security [9]. The use of sustainable and renewable energy has been emphasized worldwide. However, it is necessary to improve the security capability for the smart grid of wind power generation or solar power plants against cyber attacks. The smart grid is a power grid that can be controlled and optimized from both the supply side and the demand side [10]. Therefore, the RC, which has a simple training method and used random data for middle layer weight, is focused on to protect these smart grids. As a result, when the RC-based attack detection is used several attacks size, the attack response rate of the RC closes to 100%. Moreover, it is more resistant than the Multi Layer Perceptron (MLP) and the Support Vector Machines (SVM) methods in various attack detection methods.

The RC architecture can also be used as a climate prediction tool [11]. The Linear Inverse Modeling (LIM), which is a traditional predicting weather system, decides weather condition by checking to some important. This paper compares the performance of the LIM approach with the RC. In results, both the LIM approach and the RC have the same predictive ability when the large amount of training data is prepared, however the RC has better performance when the training data is limited. Thus, the RC is superior to the LIM by calculating the Normalized Root Mean Square Error (NRMSE), which the RC is a quarter of the LIM approach and the Anomaly Correlation Coefficient (ACC) of RC is doubled of the LIM approach. However, it is difficult to optimize the RC, and the pre-fixed weight of reservoir layer makes the prediction accuracy. It is needed to control the parameter.

From these studies, for utilizing the RC capability to prepare pre-fixed random data, we have proposed to ob-



Fig. 1. Block diagram of the ITCAM

tain unique data from LSI chips. Because the data resulting from these chip variations are physically irreproducible and always unique, they can then be used to mimic the characteristics of human individuality.

III. ITCAM-based Reservoir Computing Architecture

In this section, the ITCAM-based Reservoir Computing Architecture (IRC) is described in detail.

A. ITCAM

The ITCAM is a novel type of content addressable memory that makes it possible to reflect diverse search results by manufacturing variations in LSI chips [3]-[5]. Variations are different from each chip. Furthermore, The ITCAM can store quantified manufacturing variations in masking register, and it can output different matching address in situations when same search data are input. A block diagram of the ITCAM package is shown in Fig. 1. When the Gray code mode signal, Delay variation acquisition signal, input data, search data, and mask data are input ports. The ITCAM performs operations such as searching for word blocks, which are internal storage area. The match result and matching address are output ports.

B. ITCAM-based Reservoir Computing

In our method, the ITCAM-based Reservoir Computing (IRC) is consisted of reservoir computing architecture and the ITCAM [7]. Searching result by variation data obtains from the ITCAM is used for pre-fixed weights in the reservoir layer. In this paper, the IRC has the ITCAM layer (8 bits \times 8 entries), reservoir layer (M neurons), output layer, and input layer, which contains input data and teach data. A block diagram of the IRC is shown in Fig. 2.

Since we can select the number of neurons (M) in reservoir layer and input data size (insize), each neuron in



Fig. 2. Block diagram of the ITCAM-based RC architecture



Fig. 3. Process of making voice data to teach data

reservoir layer can receive the ITCAM data for pre-fixed weights. All neurons must be connected to the other neurons. In this paper, we connect neurons like a ring, these neurons can receive all data from the other neurons. For verifying the IRC capability, the IRC is developed Verilog-HDL language and simulated by Xilinx ISE 14.7 design suite.

IV. EXPERIMENTATION FOR VOICE LEARNING

In order to verify the operational capabilities of our proposed IRC, complex waveform and time-series data in the form of voice-recorded Japanese syllabary from "A" to "Nn" (except for "Wo") were learned by the IRC. The recorded voice data were then modified to wave teach data by fast Fourier transform (FFT) using the MAT-LAB programming and numeric computing platform. In this process, each data was multiplied by about 400 for representing waveform in detail. We choose 24 sampling frequencies. The number of neurons was set to 8, and the input size was set to 8 bits. An overview of these processes is shown in Fig. 3.

Voice data waves for teach data are shown in Fig. 4. Input data is sine wave data shown in Fig. 5.

Table I shows reservoir layer weight values [7], obtained from 10 types of FPGA chips (8 bits \times 8 entries) via ITCAM. The Xilinx Spartan6 XC6SCL160 FPGA chip us used in this study. In our experiments, the 8 output

TABLE I Reservoir layer weight values

			LCDCL	100 011	с шлі	LIC W	LIGH	T 4111	LO LD.		
		1	2	3	4	5	6	7	8	9	10
	1	183	247	234	229	2	16	0	232	14	10
	2	205	5	1	252	25	32	17	245	22	28
	3	207	7	1	249	25	31	21	246	30	31
	4	161	215	208	206	233	236	233	202	238	235
	5	200	6	250	247	60	34	58	245	59	27
	6	226	36	18	14	42	50	44	10	48	43
	7	22	84	72	67	96	108	96	62	111	102
1	0	100	242	241	222	c	14	2	221	10	10

TABLE II Relative Errors of "Ta" and "Ri" [%]

		CHIP 1	CHIP 2	CHIP 3	CHIP 4	CHIP 5	CHIP 6	CHIP 7	CHIP 8	CHIP 9	CHIP 10
ſ	Та	358	200	199	600	135	119	361	331	161	262
	Ri	1764	363	515	1463	209	680	588	592	202	363

data from the output layer are averaged after one learning process.

V. EXPERIMENTETAL RESULTS

From teaching process with all voice data, 10 chips results are shown in Fig. 6. In order to evaluate each result, the absolute relative error was calculated using the following equation.

$$Relative_error = \left| \frac{1 - result}{teach_data} \times 100 \right| [\%]$$

A. All Chips Verification

The verification results for all chips show larger values than the teach data, because the values of 2 of the 8 neurons are twice those of the other neurons. Therefore, it is considered that the obtained value is multiplied by 1.25. Thus, we multiply all averages by 0.8 for changing neuron values.

Moreover, such as "Ta" and "Ri" from all chips are shown in Table II. All of these looks very different from the teach data.

Chip 1 result of "Ri" is 1764%, which is difficult to represent as "Ri". This seems to be due to input data and teach data incompatibilities.

Wa	Ra	Ya	Ma	Ha	Na	Та	Sa	Ka	A
M	M	M	M	Am	M	M	\mathcal{M}_{γ}	Am	A.
	Ri		Mi	Hi	Ni	Chi	Shi	Ki	I
	\mathcal{M}		~ ^M	\mathcal{M}	$\mathcal{N}_{\mathcal{N}}$	Ŵ	M	Mw.	M_{\sim}
Nn	Ru	Yu	Mu	Hu	Nu	Tsu	Su	Ku	U
\mathcal{M}	\sim	Awar	m	/m/	\mathcal{M}	Mm	\mathcal{N}	MW	\mathcal{M}
	Re		Me	He	Ne	Те	Se	Ke	E
	$\mathcal{M}_{\mathcal{L}}$		M_{ν}	M	$\mathcal{M}_{\mathcal{W}}$	\wedge	M	$\mathcal{M}^{\mathcal{A}}$	MM
	Ro	Yo	Mo	Ho	No	To	So	Ko	0
	\mathcal{M}	\mathcal{M}	\mathcal{M}	Mw	MM	\mathcal{N}	MV	Mm	h

Fig. 4. Voice data wave



Fig. 5. Inputed sine wave



Fig. 7. Up: input data of "Ta", Under: input data of "Ri"

As a countermeasure, the IRC learning for "Ta" and "Ri" was performed again using the input data shown in Fig. 7. Although these data were made from the same voice data, the results were smaller than their teach data.

From the obtained result, we found that our proposed IRC could improve the relative errors, as shown in Table III.

The relative error of this second learning is 8%, thereby indicating that taking input data and teach data from the same data drastically improves the IRC learning ability.

TABLE III Errors of "Ta" and "Ri" [%]

	CHIP 1	CHIP :	2 CHIP	3	CHIP	4	CHIP	5	CHIP	6	CHIP 7	7	CHIP 8	8	CHIP 9	•	CHIP	10
Та	5		5	7		6		4		6	٤	3	e	5	6	5		6
Ri	3		7	8		3		4		2	3	3	8	3	2	2		2

Wa	Ra	Ya	Ma	Ha	Na	Ta	Sa	Ka	Α	Wa	Ra	Ya	Ma	Ha	Na	Ta	Sa	Ка	A
J.W	Ri	W	M	Ann Hi	M	Chi	My	Кi	A.	MV	Ri	M	MM	Hi	M	Chi	M Shi	Ki	A.
	_fW		$\mathcal{N}^{\mathcal{M}}$	M	M	MM	im	A.	MM		\mathcal{M}		\mathcal{M}	M	M	Ŵ	Mw	A.	M~
Nn M	Ru	Yu My M	Mu	Hu	Nu	Tsu MAM.	Su	Ku MMV	ν γ _{νν} γ	Nn M	Ru	Yu	Mu	Hu	Nu	Tsu M _M	Su	MW-	υ γ _M Λ
	Re		Me	He	Ne	Te	se A _N A	Ke N	E		Re		Me	He	Ne	Te	Se MM	Ke M	E
	Ro	Yo	Mo	Ho	No	то	so MM	Ко	о Л		Ro	Yo	Mo	Ho	No	то	so	Ko	0 1
	-* V	-/w w		(a) Ch	in1	- <u>^ v</u>		1 64	- · · · ·		· · · · ·	12 2		(b) C	hip2	· · · · ·		-7 - 601 1	
Wa	Ra A	Ya - Art	Ma	Ha	Na	Ta Mi	Sa I A A	Ка Е Л	A E A	Wa - Mu wai	Ra	Ya E. A.T	Ma	Ha E A	Na	Ta	Sa	Ka	A E A
h	Ri	EMW.	Mi	Hi	NI Ni	Chi	_/V\/\ _\ Shi	М _м	- <u>A</u>	Y	Ri	₩v	M	Hi	- MU V	Chi	J ^V V W Shi	Кi	Т
Nn	₩ 	Yu	Mu	M	Nu	Tsu	<u>∧</u> Su	Ku		Nn	Ru	Yu	Mu	M	Mu Nu	Tsu	J ^{MMM} Su	Ku	JMM U
\mathcal{M}	M	MM	Ma	M	M	Mm	A.,	Mw	1m	\mathcal{M}^{N}	M	L.M	m	M	M	Mm	<u></u>	Mw	M
	M		sh	M	Ŵ	$\mathcal{N}_{\mathcal{W}}$	M	w	Am		μh		MM	M	M	$\Lambda_{\rm v}$	M	w	N.M.
	Ro	Yo	Mo	Ho	No	To	So	M	° M		Ro	Yo	Mo	Ho	NO	To	so MM	M	° Aw
Wa	Pa	Ya	Ma	(c) Ch	ip3	Тэ	52	Ka	٨	Wa	Pa	Va	Ma	(d) Cł	nip4	То	60	Ka	
M	Ŵ	WW	M	Am	M	JAM	My	Am	Â,	M	M	W	NV	Am	M	A MA	$\mathcal{M}_{\mathcal{V}}$	A	Â
	Ri		Mi	Hi	Ni	Chi	Shi M	Кі	I An		Ri		Mi	Hi	NI	Chi	shi M	Ki M	I
Nn - N	Ru	Yu MM	Mu	Hu MM/	Nu	Tsu May	Su	Ku MM	U VM	Nn A A	Ru	Yu MM	Mu	Hu Am /	Nu	Tsu	Su	Ku	U VM
<u></u>	Re	3	ме Пда	He	Ne	Te	Se A A	Ke	E	<u></u>	Re		Ме	He	Ne AA	Te	Se A A	Ke	E
	Ro	Yo F A	Mo	Ho Ho	No No	To	So N.A	Ко	0 1		Ro	Yo F A	Mo	Ho	No	To	So A	ко Ко	• • • • •
	M	NW	<u>/</u> ~v	(e) C	hip5	- V/V	Į VĮ V	<u></u>	'hw		M	24	J VV	(f)	Chip6	_ ∕ ∕/\	/W/ V	/ L	1hrv
Wa A	Ra E A ,	Ya E Mal	Ma	Ha - A	Na - M	Ta Aug	Sa AA	Ка	A E A	Wa M	Ra - A	Υ a	Ma - A.	Ha	Na	Ta E MMN,	Sa AA	Ка	A E A
J WI	Ri Ri	Elimin	_// V ·/	Hi	Ni	Chi	Shi	-/ ⁻ / - Мака Кі			Ri	.≡ <i>J</i> ₩ŸV	Mi ■ Mi	Hi	Ni	Chi	Shi		
Nn	Ru	Yu	Mu	Hu	Nu Nu	Tsu	Su	Ku Ku		Nn	Ru	Yu	Mu	Hu	Nu Nu	N ^M W	Su Su	Ки	-/'\/ 1
N	Re	MM	Me	/W/ He	Ne	M _{Mw} Te	-A	MM-W Ke	ε ΥΜΙ	\mathcal{N}	Re	M.V.V	Me	/W/ He	Ne	Te	Se	ММ- Ке	1/m/ E
	M	Yo	Mo	M	M		M.	W.	Nym		Ro	Yo	Mo	₩ Ho	M	To	<u>NN</u>	W.	N _m
	M	\mathcal{A}	\mathcal{M}	Mr	MMM	\mathcal{M}	M	M	\mathbb{A}		\mathcal{N}	\mathbb{A}	M		M	\mathcal{M}	M	Mun	$\Lambda_{\rm v}$
Wa	Ra	Ya	Ma	(g) Cl Ha	hip/ Na	Та	Sa	Ka	A	Wa	Ra	Ya	Ma	(n) Ha	Na	Ta - Ma	Sa	Ка	A - A
M	Ri	IM.	MV ^A V	Hi	M	Chi	M _V Shi	Кi		-V-V	Ri	M	Mi Mi	Hi	 Ni	Chi	M4 Shi	Ki Ki	
	~~~~		$\sim^{M}$	M	M	Ŵ	M	M.	M.		$\sim M$		$\sim^{\mathcal{M}}$	$\mathcal{M}$	$\mathcal{A}$	Ŵ	M	Mw	M
Nn M	Ru	ru MyM	Mu	Hu	Nu MM	Tsu M _{MM}	Su A	ки MW	J.M	Nn M	Ru	Yu M	Mu	Hu	Nu	Tsu M _{MM}	Su	ки MMW	1.~~
	Re		Me	He	Ne	Te	Se AM	Ke M	E Mwm		Re		Me	He	Ne	Te	Se MM	Ke M	E Mart
	Ro	Yo	Mo	Ho	No	To	so MM	Ko M	° M		Ro	Yo	Mo	Ho	No	To	So MM	M	Â
				(i) Cł	nip9									(j)	Chip10				

Fig. 6. Voice data learning results of all chips. The vertical and horizontal axes show the amplitude and frequency [Hz], respectively.

Maximum	1764	Maximum	630
Minimum	19	Minimum	10
Average	156.1	Average	66.1
Median	28	Median	30
100 and over	I, Shi, Ta, No, Me ,Ri, Re, Wa	100 and over	Se, Ta, Me, Ri, Wa
10 or less	Ha	10 or less	Ro
Exemplary Wave	Ka, Ko, Ne, Ha, Mu, Ma	Exemplary Wave	Su, Ya, Ro
Personal Wave	I, E, hi, Ta, Chi, Me, RI, Re, Wa	a Personal Wave	Ta, No, Ho, Me, Ri, Wa
(	a) Chip 1	(k	b) Chip 2
Maximum	515	Maximum	1471
Minimum	7	Minimum	8
Average	47.1	Average	177.7
Median	28	Median	28
100 and over	Ta, Ri	100 and over	I, Shi, Ta, No, Me, Ri, Re, Wa
10 or less	Ha, Hu, Ru	10 or less	Ku, Ko, Ha, Mu
Exemplary Wave	Ki, Chi, Ne, Hu	Exemplary Wave	Ко
Personal Wave	E, Ta, Ma, Me, Ri, Wa	Personal Wave	I, Shi, Ta, Chi, Me ,RI, Re, Wa
	(c) Chip 3		d) Chip 4
Maximum	209	Maximum	680
Minimum	8	Minimum	4
Average	37.1	Average	47.0
Median	27	Median	27
100 and over	Se, Ta, Ri	100 and over	Ta No Ri
10 or less	Ha, Mi, Ra	10 or less	Ne Ha Hi Hu MI
Exemplary Wave	Su, Ma, Ya, Ra	Exemplary Wave	Chi Ne Hi Hu
Personal Wave	Ta, Ri	Personal Wave	F. Ta Ri
	(e) Chip 5		(f) Chip 6
Maximum	588	Maximum	1625
Minimum	7	Minimum	8
Average	51.6	Average	122.9
Median	29	Median	30
100 and over	Ta, No, Ri	100 and over	I, Shi, Ta, Chi, Ri, Re, Wa
10 or less	Ka, Ha	10 or less	Hi
Exemplary Wave	Chi, Me	Exemplary Wave	Ka, Ne, Hi, Hu, Ra
Personal Wave	I, Ta, Ya, Ri	Personal Wave	I, Shi, Ta, Chi, Ri, Re, Wa
	(g) Chip 7		(h) Chip 8
Maximum	234	Maximum	363
Minimum	3	Minimum	8
Average	39.7	Average	43.0
Median	27	Median	26
100 and over	Ta, Ri, Wa	100 and over	Ta, Ri
10 or less	Ku, Mu, Ro	10 or less	Mu
Exemplary Wave	I, Su, Hu, Mu, Ro	Exemplary Wave	Ku, Su, Chi, Mu
Personal Wave	Ri, Wa	Personal Wave	Ta, Ri
(	(i) Chip 9	(	(j) Chip 10

Fig. 8. Maximum [%], minimum [%], average [%], median[%], errors of 100% and over, errors of 10% or less, and the exemplary and personal results for each chip.

## B. Individual Chip Verification

Next, we will give detailed explanations of relative errors for each chip. Table 8 shows the maximum, minimum, average, and median values. Note that errors of 100% and over, 10% or less, and exemplary and personal waves are included.

# Chip 1

The Chip 1 result is shown in Table 8-(a), where it can be seen that Chip 1 has the second-largest error and only one result that is less than 10%. However, the median data value is relatively small. The results for "Ka", "Ko", "Ne", "Ha", "Mu", and "Ma" show good wave shapes.

# Chip 2

The Chip 2 result is shown in Table 8-(b). Although the average value is less than 100%, the median value, which is 30, is higher than all the other chips. In addition, "Se", "Ta", "Me", "Ri", and "Wa" are higher than 100%.

# Chip 3

The Chip 3 result is shown in Table 8-(c), where it can be seen that "Ta" and "Ri" are over 100% and the average is below 50. Additionally, "Ki", "Chi", "Ne", and "Hu" show exemplary waves, thereby indicating excellent learning results for this chip.

## Chip 4

The Chip 4 result is shown in Table 8-(d). In this case, the maximum error is 1471%, which is the third-largest value. This chip has eight results that are over 100% ("I", "Shi", "Ta", "No", "Me", "Ri", "Re", and "Wa") but it also has four results ("Ko", "Ha", "Mu", and "Ku") that are less than 10%. Additionally, the difference between the average and median values is large.

# Chip 5

The Chip 5 result is shown in Table 8-(e), where it can be seen that this chip has the smallest maximum error data and an average and median value difference of just 10%. Additionally, while results of less than 10% are noted for "Ga", "Mi", and "Ra", only "Ta" and "Ri" are the personal waves.

# Chip 6

The Chip 6 result is shown in Table 8-(f). This chip can achieve stable results in cases nor involving weak learning because it has just three errors over 100% and five errors below 10%. However, exemplary waves were only observed for "Chi", "Ne", "Hi", and "Hu".

# Chip 7

The Chip 7 result is shown in Table 8-(g). For this chip, there are only three syllables over 100%. On the other hand, the average percentage is 51, which is the third-largest value observed. The chip's exemplary waves are "Chi" and "Me", and its personal waves are "I", "Ta", "Ri", and "Ya".

# Chip 8

The Chip 8 result is shown in Table 8-(h). This chip has seven results over 100% and just one result that is less than 10%. In addition, the average percentage is over 100%. From these results, we can say that Chip 8 showed the most effective learning of all the chips tested.

#### Chip 9

The Chip 9 result is shown in Table 8-(i), where it can be seen that its maximum percentage is the second to the lowest of all chips tested. On the other hand, with a minimum of 3%, which is smaller than all the others, it can be said that Chip 9 has exemplary learning characteristics.

# Chip 10

Result of chip 10 is Table 8-(j). The Chip 10 result is shown in Table 8-(j). In this case, the maximum percentage is smaller than the others except for Chips 5 and 9. Furthermore, only one result ("Mu") is less than 10%.

From these results, we can conclude that each chip are implemented with unique characteristics. In addition, while all median percentages are about 30%, larger averages often have some larger errors. In conclusion, weight variations in the middle layer produce different results in each chip and thus create output individual.

#### VI. CONCLUSIONS

This paper shows machine learning results achieved by using the individualistic values of LSI chips for reservoir layer weight, we found that even if the same teach data are used, exemplary waveforms can be achieved for each chip by changing the weight. In addition, we found that even though the shape of the obtained personal waveforms differs depending on the chip, each chip will provide some exemplary results. In the future, we will continue to study learning data and strive to clarify individual chip characteristics.

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