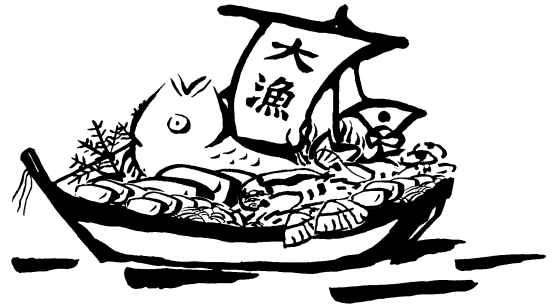# Final Call for Papers
SASIMI 2022

THE 24TH WORKSHOP ON
SYNTHESIS AND SYSTEM INTEGRATION
OF MIXED INFORMATION TECHNOLOGIES

October 24–25, 2022

Art Hotel Hirosaki City, Hirosaki, Japan
Hybrid: In-person and Online

In cooperation with:
· IEEE Council on Electronic Design Automation (CEDA) All Japan Joint Chapter
· IEEE Circuits and Systems Society (CASS) Japan Joint Chapter
· IEEE Circuits and Systems Society (CASS) Kansai Chapter
· IEEE Computer Society (CS) Kansai Chapter
· The Institute of Electronics, Information and Communication Engineers (IEICE)
· Information Processing Society of Japan (IPSJ)

Aims of the Workshop:

This workshop will provide an interchange forum on system design, design experiences, EDA, and design methodologies for both of industry and academy. Presentations on theoretical aspects, practical issues, case studies and applications are encouraged. The workshop gives an opportunity for presentation and discussion of advanced work and research. Works in progress and new ideas are also welcome. Special sessions for hot topics will be provided. Proposals for the topics are welcome.

Areas of Interest include, but are not limited to:

- System Design, Design Experiences, and Industry Experiences
- Software/Hardware Design for Embedded Systems, Cyber-Physical Systems, IoT, etc.
- New Design Methodologies for MEMS, Bioengineering, Automobile, Environment, Quantum, Reconfigurable System, etc.
- Design for Manufacturability, Reliability, Security, etc.
- Behavioral/Logic/Layout Synthesis
- Test, Verification, and Simulation
- Analog and Mixed-Signal Design
- Machine Learning, AI, and Brain-Inspired Computing.

Submission of Papers:

Prospective authors are invited to submit short papers of 2 pages, or full papers* of 3 to 6 pages, electronically via the web site below. Detailed instructions for paper submission will be available at the web site. Official language is English.   * Best Paper Awards will be selected from full papers.

Key Dates:

Submission due date (extended)	~~June 3 (Fri)~~ **June 13 (Mon), 2022**
Notification of Acceptance	**August 5 (Fri), 2022**
Final paper due date	**September 9 (Fri), 2022**

For more and latest information: `https://sasimi.jp/`

Highlight of the Program

Keynote Speeches

- Sudeep Pasricha (Colorado State University), "Hardware/Software Codesign for Machine Learning Acceleration with Silicon Photonics"
- Rolf Drechsler (University of Bremen), "One is not Enough: Using Hybrid Proof Engines for Polynomial Formal Verification"

Invited Talks

- Jen-Ming Wu (National Tsing Hua University/Hon Hai Research Institute), "Challenges and Opportunities for New Radio New Type Communications for 5G and Beyond"
- Shigetaka Kumashiro (Kyoto Institute of Technology), "Utilization of Dominant Time Constant Information to Improve the Efficiency of Power Device Simulation"
- Kunio Kojima (The University of Tokyo), "Design and Development of Electronic Devices for Driving, Measuring and Controlling Humanoid Robots"

Conference Venue

Hirosaki City is in a west side of the Aomori Pref. and developed as a castle town of the Hirosaki clan and the central city of the Tsugaru region. Hirosaki City has the largest apple production amount in Japan, accounting for about 20%．The autumn leaves in Hirosaki Park, where 1,000 maple trees and 2600 cherry trees turn red, are in full bloom from mid October to mid November. You can also enjoy the apple picking experience from mid-October to mid-November at Hirosaki City Apple Park.

Hirosaki Castle

Neputa Village

Iwaki Mountain

Apple, Japanese sake

Zen temples, Five-storied pagoda

Art Hotel Hirosaki City

Questions should be directed to:

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For more and latest information: `https://sasimi.jp/`